

011N03T-D5

30V N-Channel Super Trench Power MOSFET

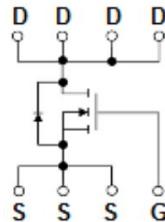


康比電子
HORNBY ELECTRONIC

<p>Description</p> <p>011N03T-D5 is uses Super Trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications</p>	<p>MAIN CHARACTERISTICS</p> <table border="1" data-bbox="933 332 1394 460"> <tr> <td>ID</td><td>260A</td></tr> <tr> <td>VDSS</td><td>30V</td></tr> <tr> <td>R_{DS(ON)Typ} (at VGS=10V)</td><td>0.85mΩ</td></tr> </table>	ID	260A	VDSS	30V	R _{DS(ON)Typ} (at VGS=10V)	0.85mΩ
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<p>General Features</p> <ul style="list-style-type: none"> ● Shielded-gate technology ● Low on-resistance and low gate-charge ● RoHS compliant 	<p>Application</p> <ul style="list-style-type: none"> ● High-frequency DC-to-DC convertors ● Motor controllers ● Battery management and protection ● Server power 						



PDFN5×6-8L



Package Marking and Ordering Information

Device Marking	Device	Device Package	Quantity
011N03T	011N03T-D5	PDFN5×6-8L	5000 pcs/Tape & Reel

Absolute maximum ratings

Symbol	Parameter		Limit	Unit
V _{DSS}	Drain-source voltage		30	V
V _{GSS}	Gate-Source Voltage		±20	V
I _D	Drain Current -continuous(TC=25°C) (1)		260	A
	Drain Current -continuous(TC=100°C) (1)		164	
I _{DM}	Drain Current-Pulsed (2)		1040	A
P _D	Power Dissipation	T _c =25°C	114	W
E _{AS}	Single pulsed avalanche energy (3)		1090	mJ
T _J , T _{STG}	Operating and Storage Temperature Range		-50~150	°C

Thermal Characteristics

Symbol	Parameter	Value	Units
R _{θJC}	Thermal Resistance, Juction-to-Case	1.1	°C/W
R _{θJA}	Thermal Resistance, Juction-to-Ambient (4)	45	°C/W

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Electrical Characteristics $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max	Unit
Static Characteristics						
BV_{DSS}	Drain Source breakdown voltage	$V_{GS}=0V, I_D=250\mu A, T_J=25^\circ C$	30	-	-	V
Id_{SS}	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V$	-	-	1	μA
I_{GSS}	Gate-to-Source Forward Leakage	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1	-	2	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=50A$	-	0.85	1.1	$m\Omega$
		$V_{GS}=4.5V, I_D=50A$	-	1.2	1.55	$m\Omega$
R_G	Gate Resistance	$f = 1 MHz$	-	1.3	-	Ω

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 15V$ $f = 1MHz$	-	6190	-	pF
C_{oss}	Output Capacitance		-	3000	-	pF
C_{rss}	Reverse Transfer Capacitance		-	300	-	pF

SWITCHING Characteristics

$T_{D(on)}$	Turn-on Delay Time	$V_{DD} = 15V$ $V_{GS} = 10V$ $R_G = 3\Omega$ $I_D = 30A$	-	22	-	ns
T_r	Turn-on Rise Time		-	20	-	ns
$T_{D(off)}$	Turn-off Delay Time		-	83	-	ns
T_f	Turn-off Fall Time		-	25	-	ns
Q_g	Total Gate Charge	$V_{DD} = 15V$ $V_{GS} = 10V$ $I_D = 30A$	-	93	-	nC
Q_{gs}	Gate Source Charge		-	14	-	nC
Q_{gd}	Gate Drain Charge		-	13	-	nC

Drain-Source Diode Characteristics and Maximum Ratings

V_{SD}	Drain-Source Diode Forward Voltage	$I_S = 50A, V_{GS} = 0V$	-	-	1.2	V
T_{rr}	Reverse Recovery Time	$I_S = 30A, V_{GS} = 0V$ $di/dt = 100A/\mu s$	-	55	-	ns
Q_{rr}	Reverse Recovery Charge		-	100	-	nC

Notes:

1. Rated according to R_{eJC}
2. Limited by maximum T_J
3. Starting $T_J = 25^\circ C$, $I_{AS} = 40A$, $L = 0.5mH$, $V_{DD} = 20V$, $V_{GS} = 10V$
4. Surface-mounted on 1 inch² FR4 board, 2 oz Cu

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (Curves)

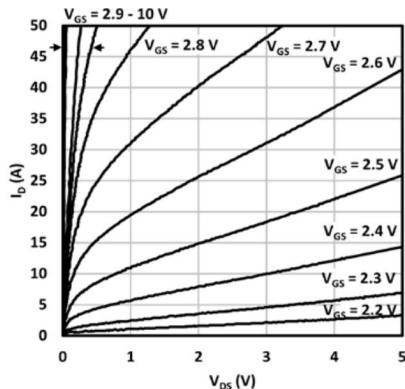


Fig. 1 Output characteristics

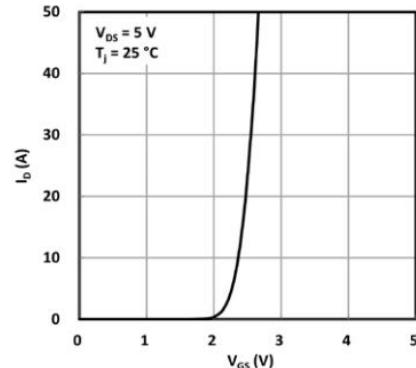


Fig. 2 Transfer characteristics

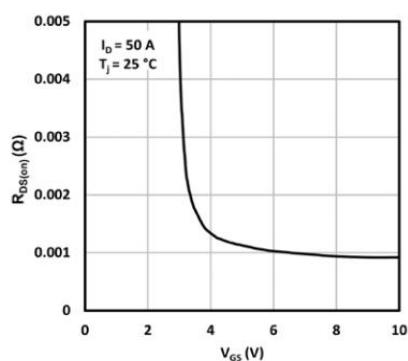


Fig. 3 On-resistance vs. gate voltage

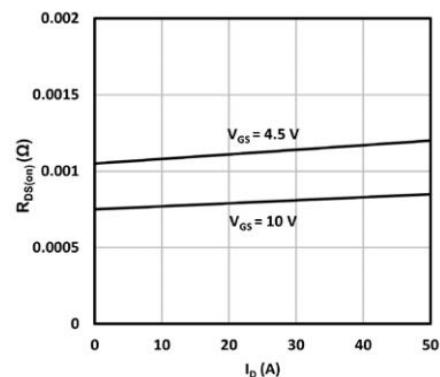


Fig. 4 On-resistance vs. drain current

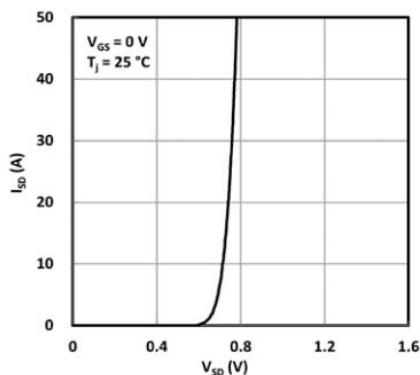


Fig. 5 Source-to-drain diode forward characteristics

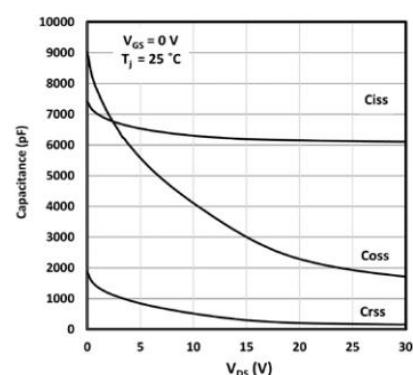


Fig. 6 Capacitance vs. drain-to-source voltage

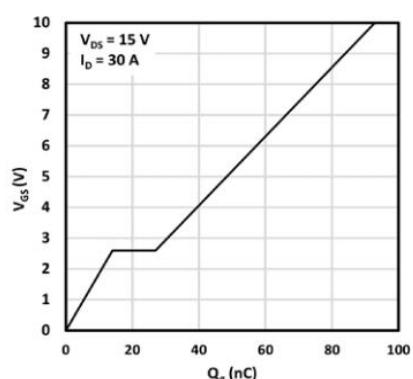


Fig. 7 Gate-to-source voltage vs. gate charge

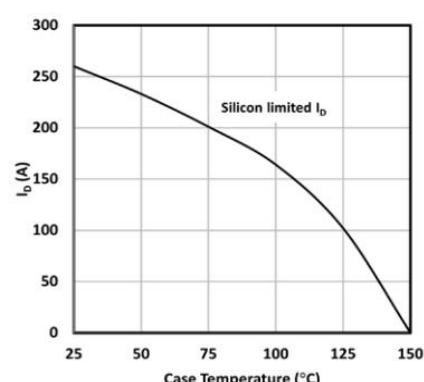


Fig. 8 Maximum drain current vs. case temperature

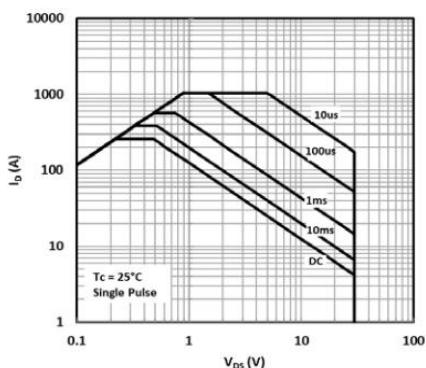


Fig. 9 Safe operating area

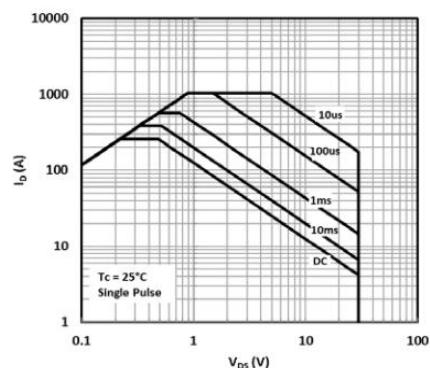
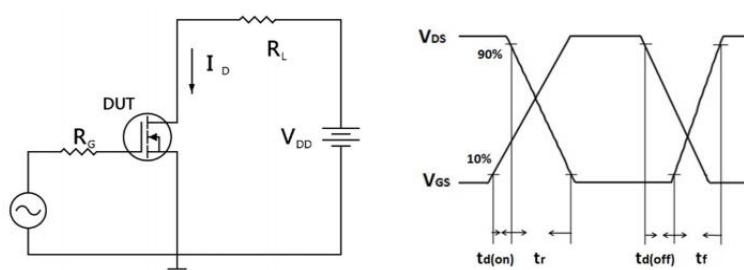
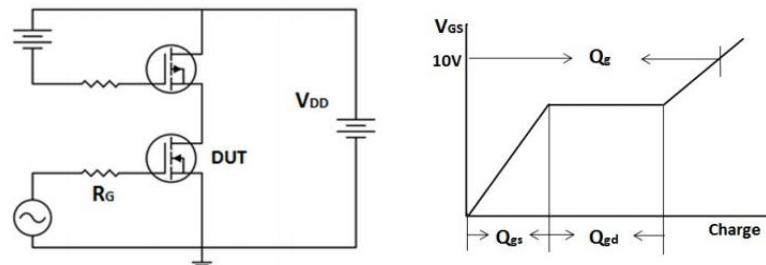


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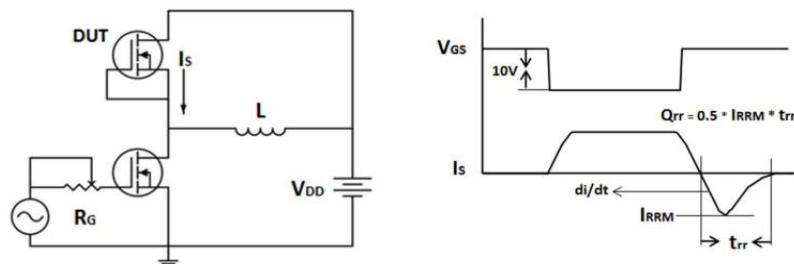
Test Circuits and Waveforms



Resistive switching time test circuit & waveforms



Gate charge test circuit & waveform



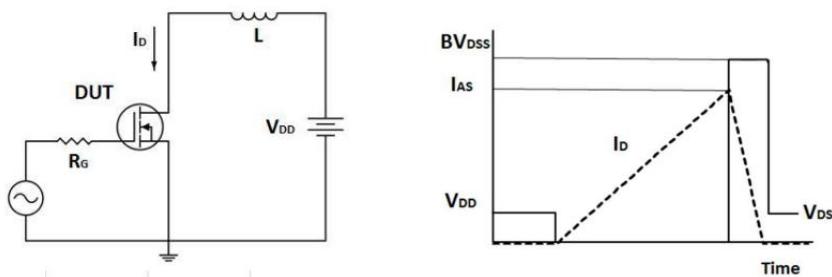
Peak diode recovery dv/dt test circuit & waveforms

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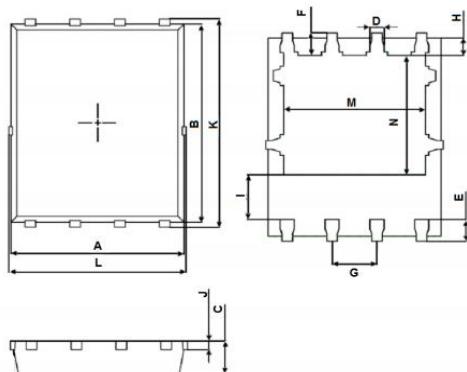


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Unclamped inductive switching test circuit & waveforms

PDFN5*6-8L Package Information



PDFN5×6-8L		
Dimension	Min.	Max.
A	4.824	4.976
B	5.674	5.826
C	0.900	1.000
D	0.350	0.450
E	0.559	0.711
F	0.574	0.726
G	1.250	1.290
H	0.424	0.576
I	1.190	1.390
J	0.154	0.354
K	5.974	6.126
L	4.944	5.096
M	3.910	4.110
N	3.375	3.575

