

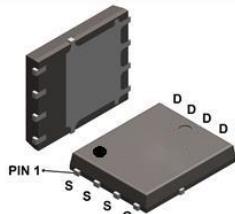
029N06T-D5

60V N-Channel Super Trench Power MOSFET

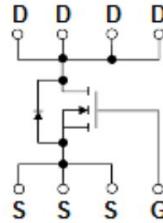


康比電子
HORNBY ELECTRONIC

<p>Description</p> <p>029N06T-D5 is uses Super Trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications</p>	<p>MAIN CHARACTERISTICS</p> <table border="1" data-bbox="928 325 1389 460"> <tr> <td>ID</td><td>125A</td></tr> <tr> <td>VDSS</td><td>60v</td></tr> <tr> <td>R_{DS(ON)Typ} (at VGS=10V)</td><td>2.4mΩ</td></tr> </table>	ID	125A	VDSS	60v	R _{DS(ON)Typ} (at VGS=10V)	2.4mΩ
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VDSS	60v						
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<p>General Features</p> <ul style="list-style-type: none"> ● Advanced shielded-gate technology ● Ultra-low on-resistance and gate-charge ● RoHS compliant 	<p>Application</p> <ul style="list-style-type: none"> ● DC/DC Converter ● Motor controllers ● Battery-driven electronic products, electrical equipment and machines 						



PDFN5×6-8L



Package Marking and Ordering Information

Device Marking	Device	Device Package	Quantity
029N06T	029N06T-D5	PDFN5×6-8L	5000 pcs/Tape & Reel

Absolute maximum ratings

Symbol	Parameter		Limit	Unit
V _{DSS}	Drain-source voltage		60	V
V _{GSS}	Gate-Source Voltage		±20	V
I _D	Drain Current -continuous(TC=25°C) (1)		125	A
	Drain Current -continuous(TC=100°C) (1)		79	
I _{DM}	Drain Current-Pulsed (4)		240	A
P _D	Power Dissipation	T _c =25°C	86	W
E _{AS}	Single pulsed avalanche energy (5)		130	mJ
T _J , T _{STG}	Operating and Storage Temperature Range		-50~150	°C

Thermal Characteristics

Symbol	Parameter	Value	Units
R _{θJC}	Thermal Resistance, Juction-to-Case	1.45	°C/W
R _{θJA}	Thermal Resistance, Juction-to-Ambient (3)	45	°C/W

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Symbol	Parameter	Conditions	Min.	Typ.	Max	Unit
Static Characteristics						
BV_{DSS}	Drain Source breakdown voltage	$V_{GS}=0V, I_D=250\mu A, T_J=25^\circ C$	60	-	-	V
Id_{SS}	Zero Gate Voltage Drain Current	$V_{DS}=60V, V_{GS}=0V$	-	-	1	μA
I_{GSS}	Gate-to-Source Forward Leakage	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.1	-	2.2	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=20A$	-	2.4	2.9	$m\Omega$
		$V_{GS}=4.5V, I_D=20A$	-	3.4	4.4	$m\Omega$

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1.0MHz$	-	3467	-	pF
C_{oss}	Output Capacitance		-	1400	-	pF
C_{rss}	Reverse Transfer Capacitance		-	50	-	pF

SWITCHING Characteristics

$T_{D(on)}$	Turn-on Delay Time	$V_{DD} = 30V$ $V_{GS} = 10V$ $R_G = 3\Omega$ $I_D = 15A$	-	23	-	ns
T_r	Turn-on Rise Time		-	62	-	ns
$T_{D(off)}$	Turn-off Delay Time		-	105	-	ns
T_f	Turn-off Fall Time		-	28	-	ns
Q_g	Total Gate Charge	$V_{DD} = 30V$ $V_{GS} = 10V$ $I_D = 30A$	-	54	-	nC
Q_{gs}	Gate Source Charge		-	8	-	nC
Q_{gd}	Gate Drain Charge		-	12	-	nC

Drain-Source Diode Characteristics and Maximum Ratings

V_{SD}	Drain-Source Diode Forward Voltage	$I_S = 30A, V_{GS} = 0V$	-	-	1.2	V
T_{rr}	Reverse Recovery Time	$I_S = 30A, V_{GS} = 0V$ $di/dt = 100A/\mu s$	-	34	-	ns
Q_{rr}	Reverse Recovery Charge		-	50	-	nC

Notes:

1. Rated according to $R_{θ,JC}$
2. Rated according to $R_{θ,JA}$
3. Surface-mounted on 1 inch² FR4 board, 2 oz Cu
4. Limited by maximum T_J
5. Starting $T_J = 25^\circ C$, $L = 0.1mH$, $V_{DD} = 30V$, $V_{GS} = 10V$
6. Pulse width limited by maximum T_J

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (Curves)

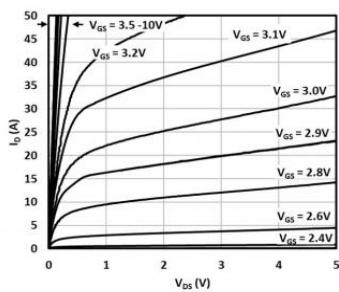


Fig. 1 Output characteristics

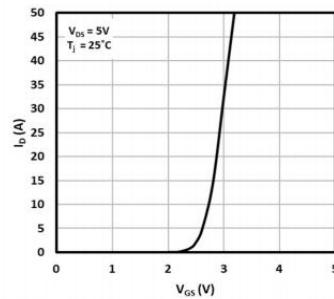


Fig. 2 Transfer characteristics

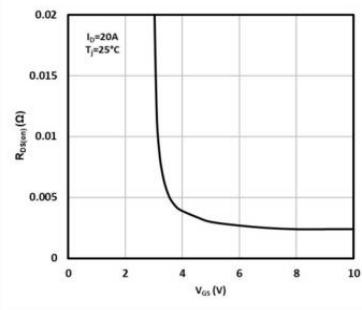


Fig. 3 On-resistance vs. gate voltage

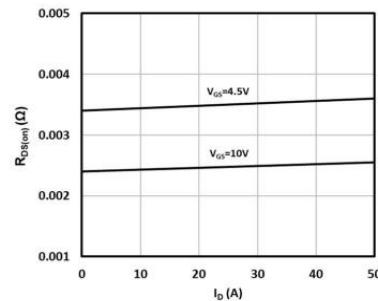


Fig. 4 On-resistance vs. drain current

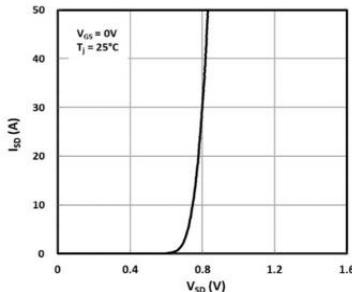


Fig. 5 Source-to-drain diode forward characteristics

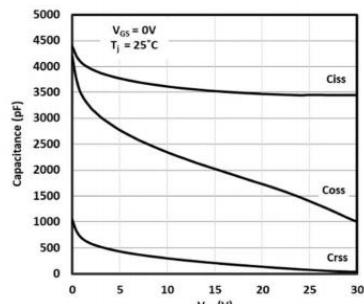


Fig. 6 Capacitance vs. drain-to-source voltage

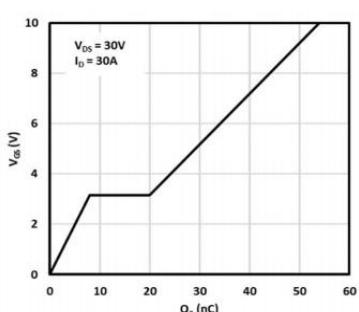


Fig. 7 Gate-to-source voltage vs. gate charge

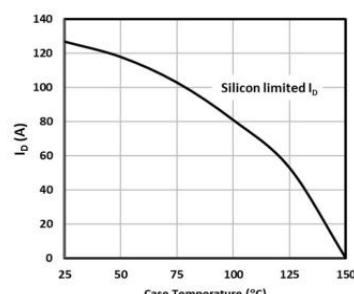


Fig. 8 Maximum drain current vs. case temperature

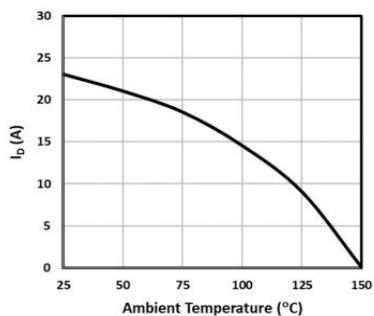
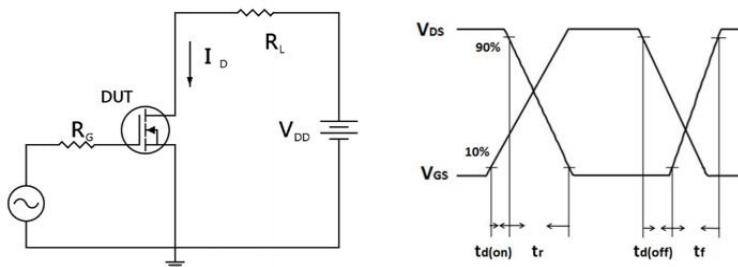
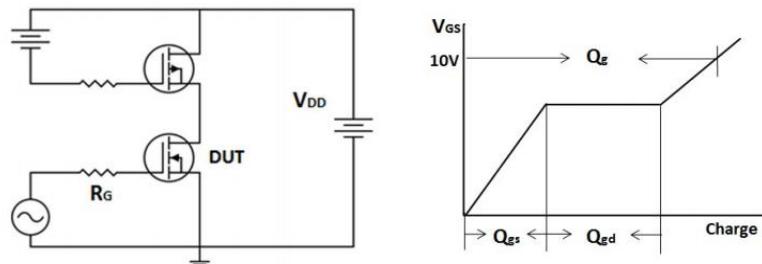


Fig. 9 Maximum drain current vs. ambient temperature

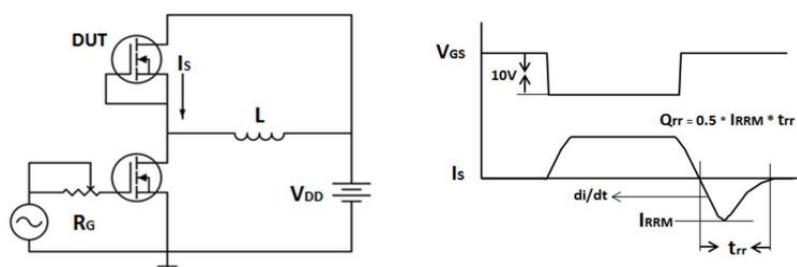
Test Circuits and Waveforms



Resistive switching time test circuit & waveforms



Gate charge test circuit & waveform



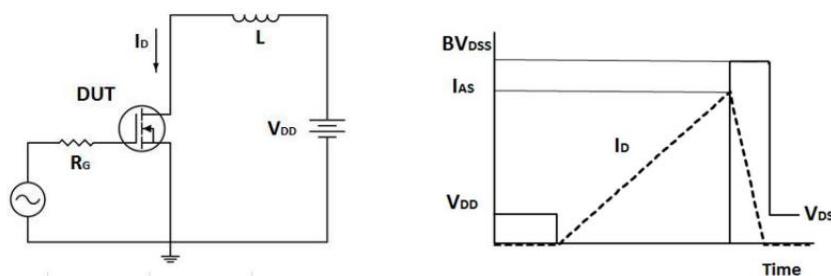
Peak diode recovery dv/dt test circuit & waveforms

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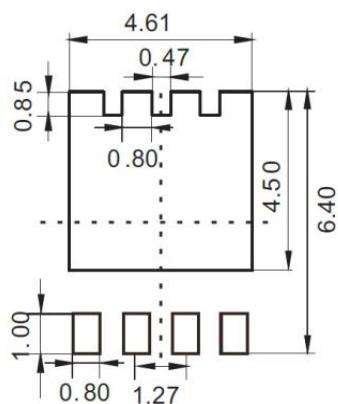
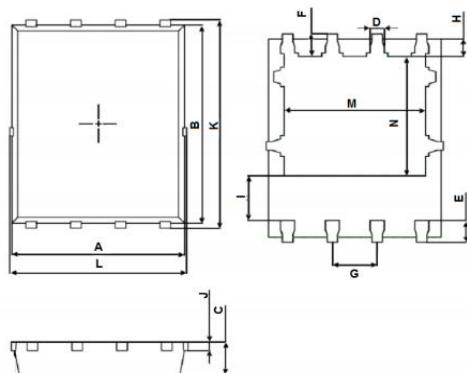


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Unclamped inductive switching test circuit & waveforms

PDFN5*6-8L Package Information



PDFN5×6-8L		
Dimension	Min.	Max.
A	4.824	4.976
B	5.674	5.826
C	0.900	1.000
D	0.350	0.450
E	0.559	0.711
F	0.574	0.726
G	1.250	1.290
H	0.424	0.576
I	1.190	1.390
J	0.154	0.354
K	5.974	6.126
L	4.944	5.096
M	3.910	4.110
N	3.375	3.575