

036N04T-D5

## ***40V N-Channel Super Trench Power MOSFET***



康比電子  
HORNBY ELECTRONIC

<p><b>Description</b></p> <p>036N04T-D5 uses Super Trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications</p>	<p><b>MAIN CHARACTERISTICS</b></p> <table border="1" data-bbox="946 336 1400 453"> <tr> <td>ID</td><td>96A</td></tr> <tr> <td>VDSS</td><td>40V</td></tr> <tr> <td>R<sub>DS(ON)Typ</sub> (at V<sub>GS</sub>=10V)</td><td>3.0mΩ</td></tr> </table>	ID	96A	VDSS	40V	R <sub>DS(ON)Typ</sub> (at V <sub>GS</sub> =10V)	3.0mΩ
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<p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● Advanced shielded-gate technology</li> <li>● Ultra-low on-resistance and gate-charge</li> <li>● RoHS compliant</li> </ul>	<p><b>Application</b></p> <ul style="list-style-type: none"> <li>● DC/DC Converter</li> <li>● Motor controllers</li> <li>● Battery-driven electronic products, electrical equipment and machines</li> </ul>						

## **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Quantity
036N04T	036N04T-D5	PDFN5×6-8L	5000 pcs/Tape & Reel

## Absolute maximum ratings

Symbol	Parameter	Limit	Unit
V <sub>DSS</sub>	Drain-source voltage	40	V
V <sub>GSS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current -continuous(TC=25°C) (1)	96	A
	Drain Current -continuous(TC=100°C) (1)	61	
I <sub>DM</sub>	Drain Current-Pulsed (4)	160	A
P <sub>D</sub>	Power Dissipation	56.8	W
E <sub>AS</sub>	Single pulsed avalanche energy (5)	93	mJ
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-50~150	°C

### **Thermal Characteristics**

Symbol	Parameter	Value	Units
R <sub>θ_JC</sub>	Thermal Resistance, Juction-to-Case	2.2	°C/W
R <sub>θ_JA</sub>	Thermal Resistance, Juction-to-Ambient (3)	45	°C/W

# 036N04T-D5

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Electrical Characteristics  $T_C=25^\circ C$  unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max	Unit
<b>Static Characteristics</b>						
$BV_{DSS}$	Drain Source breakdown voltage	$V_{GS}=0V, I_D=250\mu A, T_J=25^\circ C$	40	-	-	V
$Id_{SS}$	Zero Gate Voltage Drain Current	$V_{DS}=40V, V_{GS}=0V$	-	-	1	$\mu A$
$I_{GSS}$	Gate-to-Source Forward Leakage	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA

### On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.1	-	2.2	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=20A$	-	3.0	3.6	$m\Omega$
		$V_{GS}=4.5V, I_D=20A$	-	4.0	5.2	$m\Omega$

### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 20V$ $f = 1.0MHz$	-	1802	-	pF
$C_{oss}$	Output Capacitance		-	710	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	76	-	pF

### SWITCHING Characteristics

$T_{D(on)}$	Turn-on Delay Time	$V_{DD} = 20V$ $V_{GS} = 10V$ $R_G = 3\Omega$ $I_D = 20A$	-	4	-	ns
$T_r$	Turn-on Rise Time		-	5	-	ns
$T_{D(off)}$	Turn-off Delay Time		-	20	-	ns
$T_f$	Turn-off Fall Time		-	5	-	ns
$Q_g$	Total Gate Charge	$V_{DD} = 20V$ $V_{GS} = 10V$ $I_D = 20A$	-	22.9	-	nC
$Q_{gs}$	Gate Source Charge		-	3.1	-	nC
$Q_{gd}$	Gate Drain Charge		-	4	-	nC

### Drain-Source Diode Characteristics and Maximum Ratings

$V_{SD}$	Drain-Source Diode Forward Voltage	$I_S = 50A, V_{GS} = 0V$	-	-	1.2	V
$T_{rr}$	Reverse Recovery Time	$I_S = 20A, V_{GS} = 0V$ $di/dt = 100A/\mu s$	-	47	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	36	-	nC

### Notes:

1. Rated according to  $R_{\theta,JC}$
2. Rated according to  $R_{\theta,JA}$
3. Surface-mounted on 1 inch<sup>2</sup> FR4 board, 2 oz Cu
4. Limited by maximum  $T_J$
5. Starting  $T_J = 25^\circ C$ ,  $L = 0.1mH$ ,  $V_{DD} = 30V$ ,  $V_{GS} = 10V$
6. Pulse width limited by maximum  $T_J$

## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (Curves)

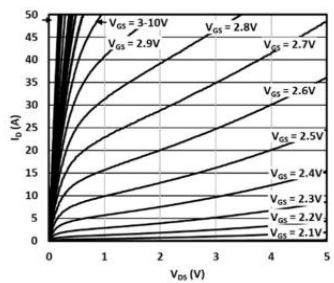


Fig. 1 Output characteristics

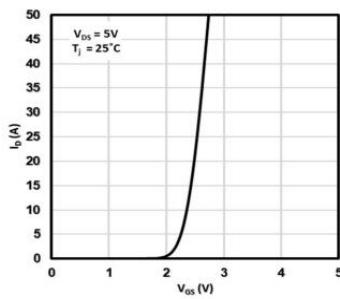


Fig. 2 Transfer characteristics

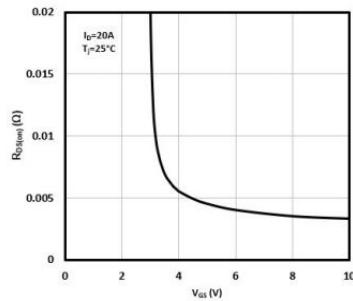


Fig. 3 On-resistance vs. gate voltage

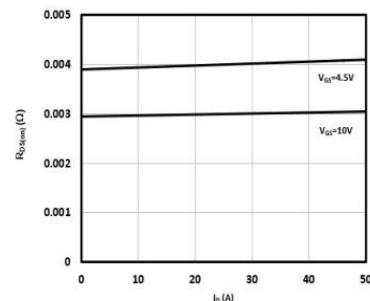


Fig. 4 On-resistance vs. drain current

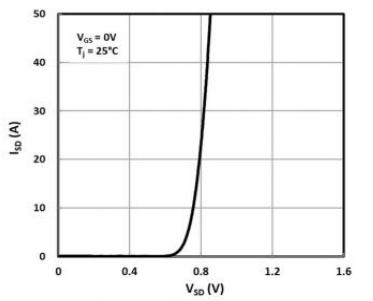


Fig. 5 Source-to-drain diode forward characteristics

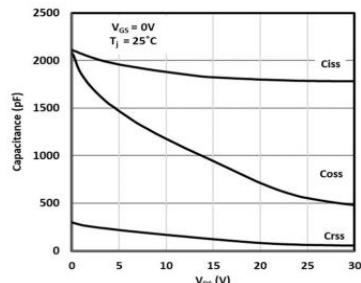


Fig. 6 Capacitance vs. drain-to-source voltage

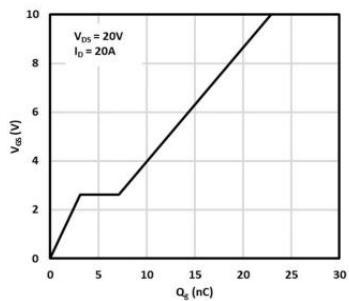


Fig. 7 Gate-to-source voltage vs. gate charge

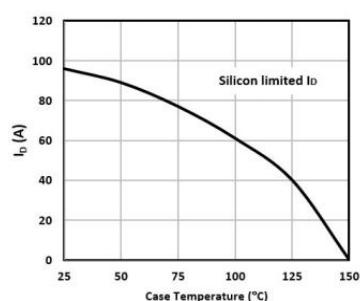


Fig. 8 Maximum drain current vs. case temperature

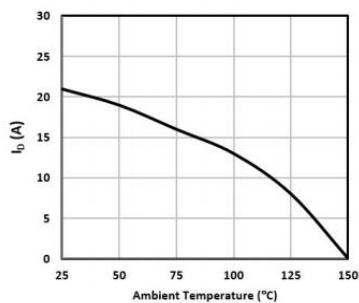
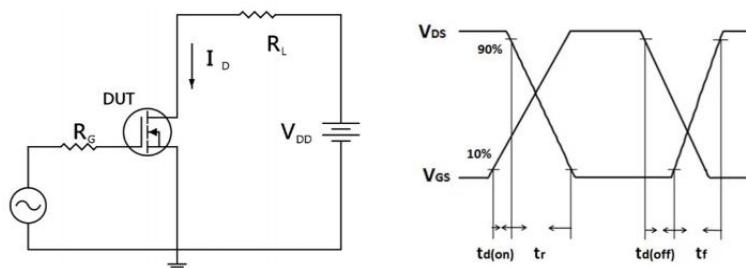
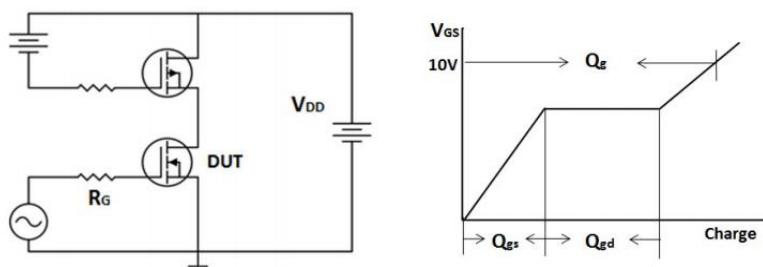


Fig. 9 Maximum drain current vs. ambient temperature

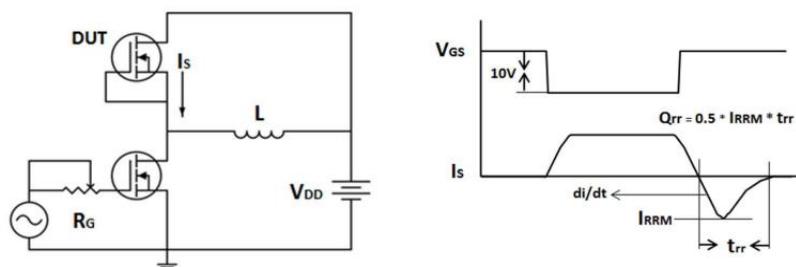
## Test Circuits and Waveforms



Resistive switching time test circuit &amp; waveforms

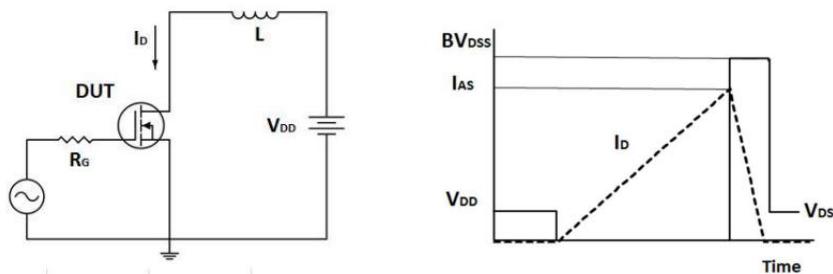


Gate charge test circuit &amp; waveform

Peak diode recovery  $dv/dt$  test circuit & waveforms

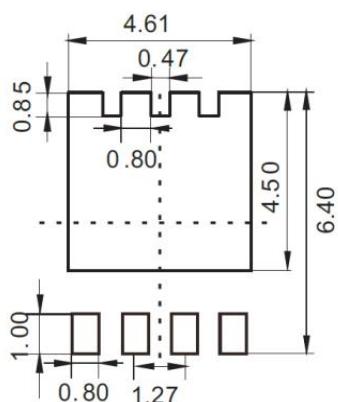
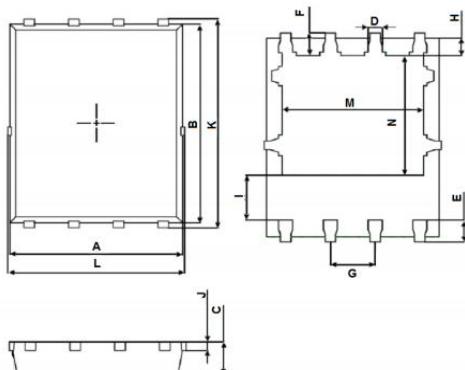
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Unclamped inductive switching test circuit & waveforms

## PDFN5\*6-8L Package Information



PDFN5×6-8L		
Dimension	Min.	Max.
A	4.824	4.976
B	5.674	5.826
C	0.900	1.000
D	0.350	0.450
E	0.559	0.711
F	0.574	0.726
G	1.250	1.290
H	0.424	0.576
I	1.190	1.390
J	0.154	0.354
K	5.974	6.126
L	4.944	5.096
M	3.910	4.110
N	3.375	3.575