YK3404A

N-Channel Enhancement Mode Field Effect Transistor

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General Description

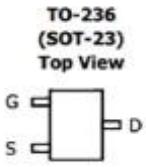
The YK3404A uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

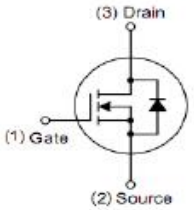
Application

- PWM application
- Load switch
- Power management

Features

- V_{DS} = 30V,I_D = 5.8A
  $R_{DS(ON)}$ <40mΩ @V_{GS}=4.5V
  $R_{DS(ON)}$ <28mΩ @V_{GS}=10V
- High power and current handing capability
- Lead free product is acquired
- Surface mount package





Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
A49T	YK3404A	SOT-23	Ø180mm	8mm	3000 units

Absolute Maximum Ratings (T_A=25°Cunless otherwise noted)

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DSS}	30	V
Gate-Source Voltage	V_{GSS}	±20	V
Drain Current-Continuous V_{GS}=4.5V，@Ta=25°C	I_D	5.8	A
Drain Current –Pulsed[Note1]	I_{DM}	20	A
Maximum Power Dissipation @Ta=25°C	P_D	1.4	W
Operating Junction and Storage Temperature Range	T_J	-55 ~ +150	°C

Thermal Characteristics

Thermal Resistance,Junction-to-Ambient[Note2]	$R_{\theta JA}$	89	°C/W

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Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}= 0V, I_D = 250μA	30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 30V, V_{GS} = 0V	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS} = ±20V,V_{DS} = 0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}= V_{GS}, I_{DS}= 250μA	1.2	1.6	2.4	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}= 10V, I_{DS}= 5A	-	20	28	mΩ
		V_{GS}= 4.5V, I_{DS}= 4A	-	27	40	
Forward Transconductance	g_{FS}	V_{DS} =5V, I_D =5A	-	15	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C_{iss}	VDS=15V,VGS=0V, F=1.0MHz	-	485	-	pF
Output Capacitance	C_{oss}		-	65	-	
Reverse Transfer Capacitance	C_{rss}		-	54	-	
Switching Characteristics (Note 4)						
Turn-on Delay Time	$T_{d(on)}$	VDD=15V, RL=3Ω VGS=10V,RGEN=3Ω	-	5	-	ns
Turn-on Rise Time	T_r		-	3	-	
Turn-Off Delay Time	$T_{d(OFF)}$		-	15	-	
Turn-Off Fall Time	T_f		-	3.5	-	
Total Gate Charge	Q_g	VDS=15V,ID=5A, VGS=10V	-	13	-	nC
Gate-Source Charge	Q_{gs}		-	2	-	
Gate-Drain Charge	Q_{gd}		-	2.6	-	
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	VGS=0V,IS=5A Tj = 25°C	-	-	1.2	V
Diode Forward Current (Note 2)	I_S		-	-	5.8	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics

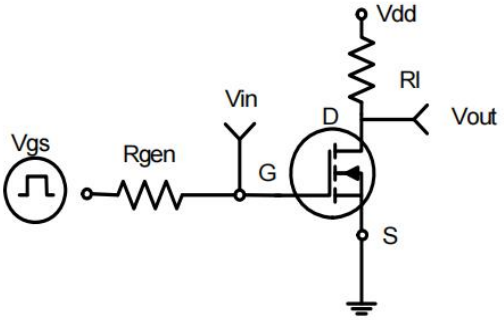


Figure 1:Switching Test Circuit

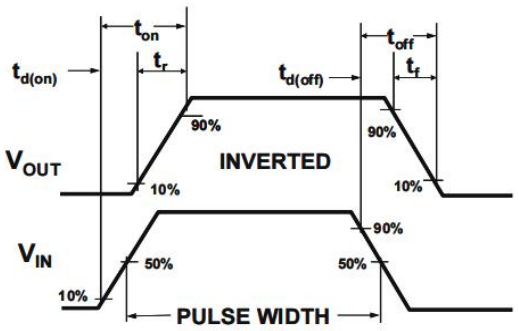


Figure 2:Switching Waveforms

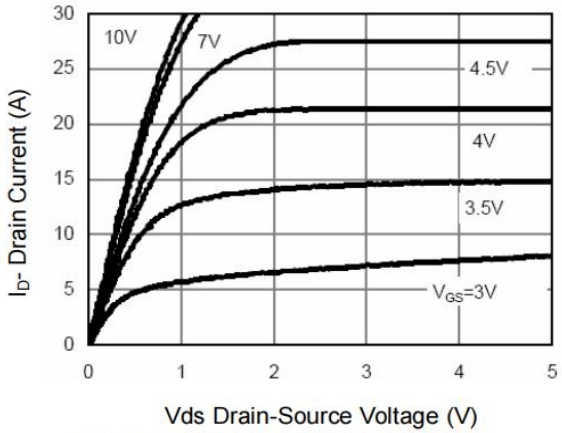


Figure 3 Output Characteristics

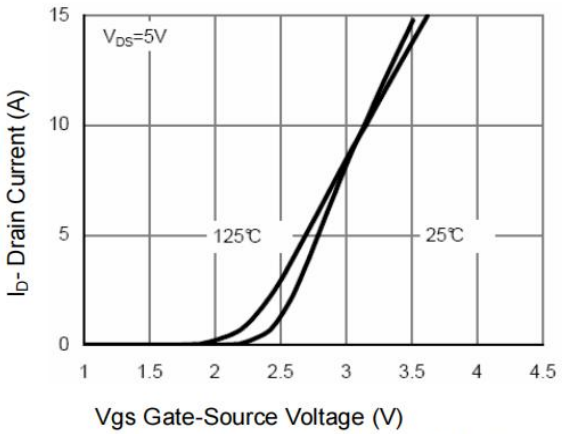


Figure 4 Transfer Characteristics

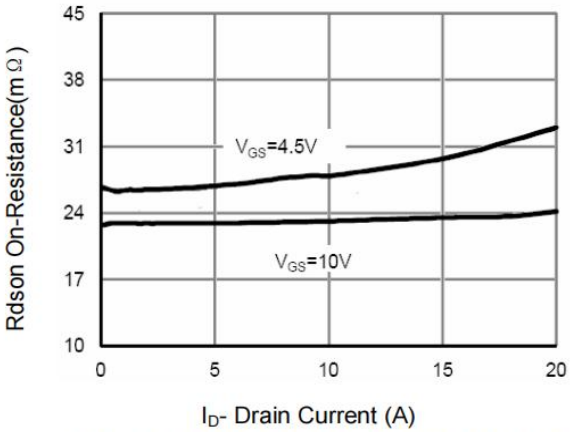


Figure 5 Drain-Source On-Resistance

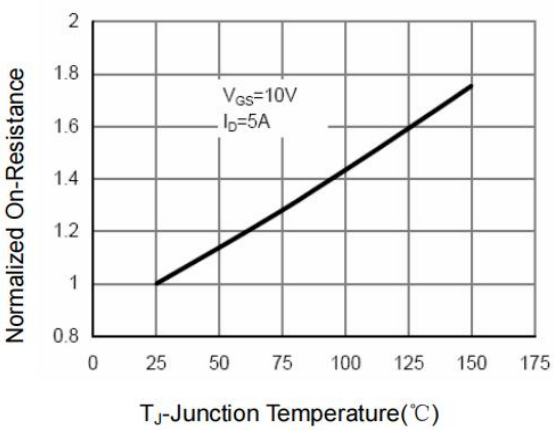


Figure 6 Drain-Source On-Resistance

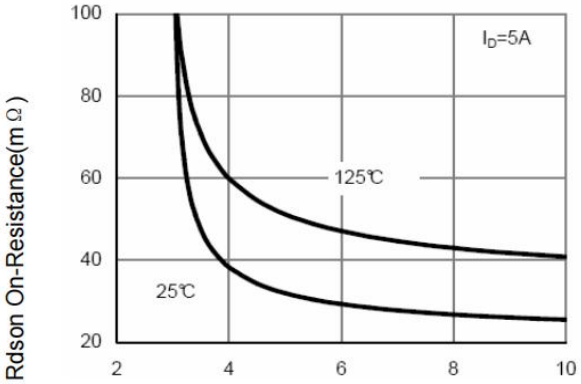


Figure7 Rdson vs Vgs

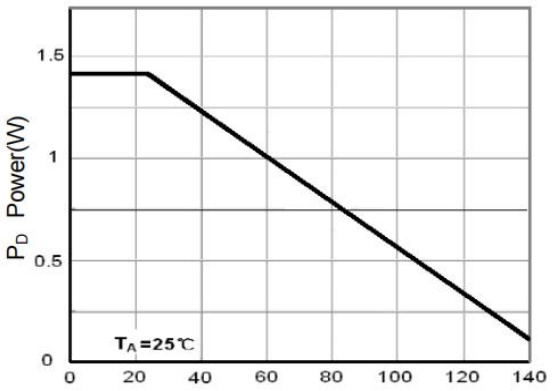


Figure 8 Power Dissipation

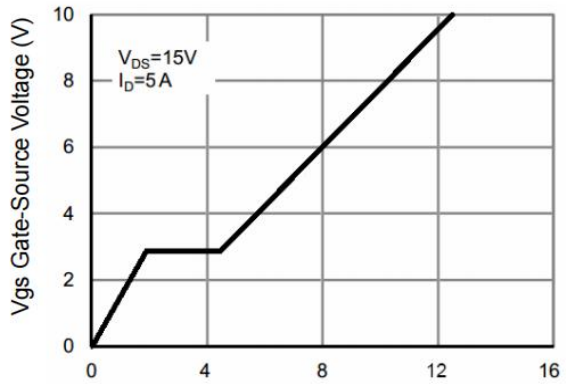


Figure 9 Gate Charge

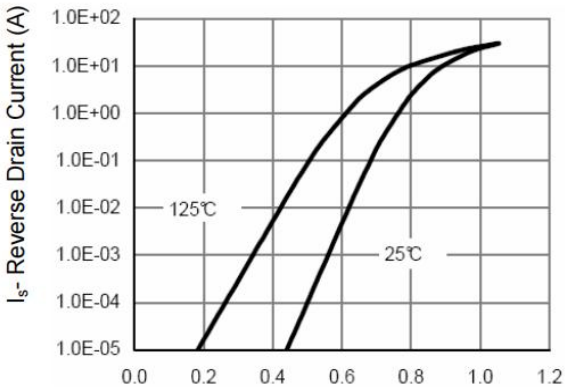


Figure 10 Source- Drain Diode Forward

Figure 9 Gate Charge

Figure 10 Source- Drain Diode Forward

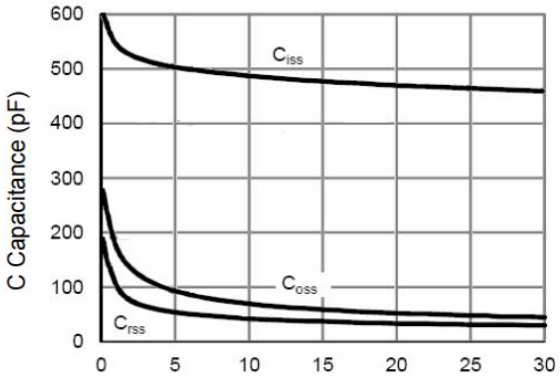


Figure 11 Capacitance vs Vds

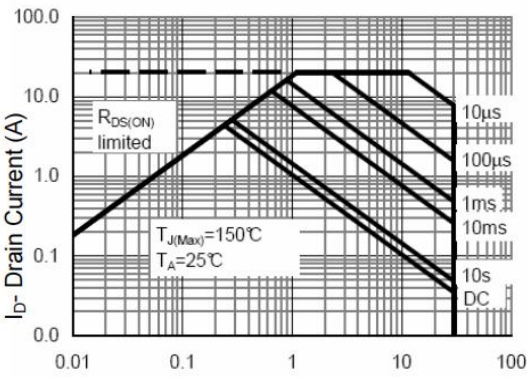


Figure 12 Safe Operation Area

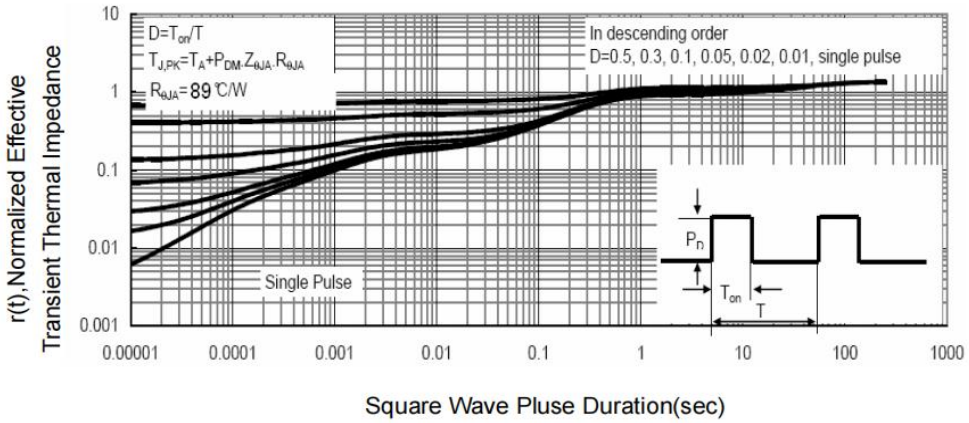
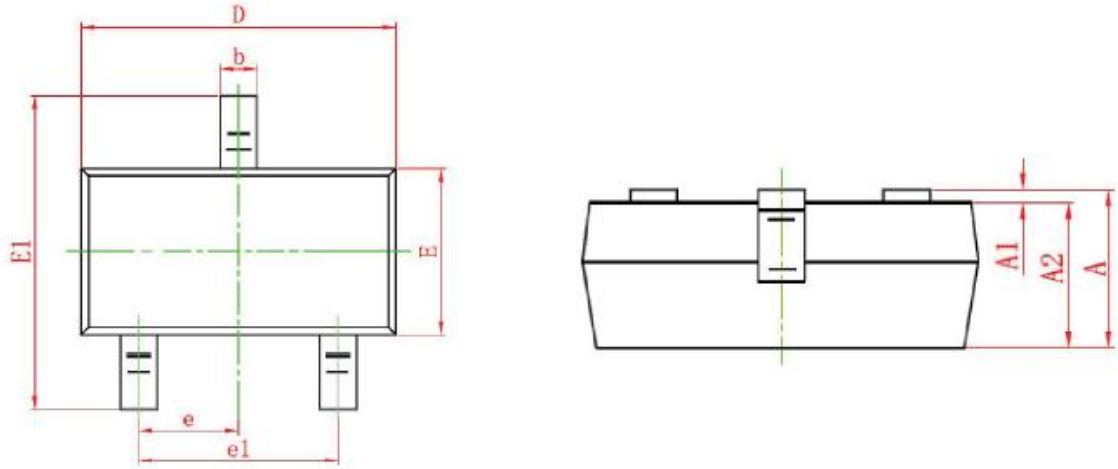
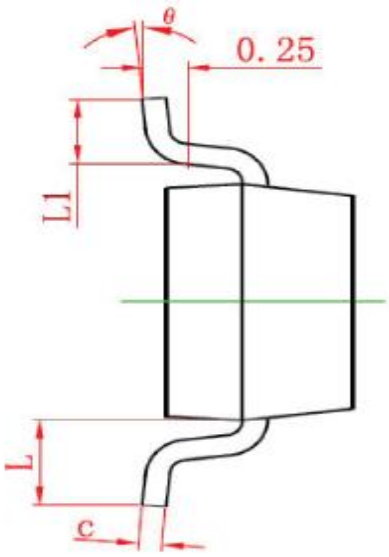


Figure 13 Normalized Maximum Transient Thermal Impedance

SOT-23 Package Information





Symbol	Dimensions in Millimeters	
	MIN	MAX
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
C	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950 TYP	
e1	1.800	2.000
L	0.550 REF	
L1	0.300	0.500
θ	0°	8°