

YK760N20HB

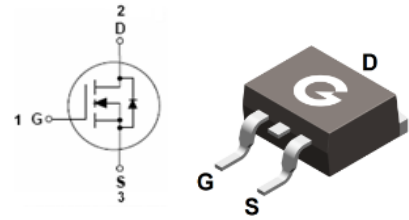
Features

- Proprietary new Trench technology
- Low gate charge minimize switching loss
- Fast recovery body diode

HF

Mechanical Data

- Case: TO-263
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208



TO-263

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
YK760N20HB	TO-263	50 pcs / Tube & 800 pcs / Tape & Reel	760N20HB

Maximum Ratings (@ $T_c = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	200	V
Gate-to-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current ($T_c = 25^\circ\text{C}$)	I_D	30	A
Continuous Drain Current ($T_c = 100^\circ\text{C}$)		21	A
Pulsed Drain Current ($V_{GS} = 10\text{V}$) ^{*1, 2}	I_{DM}	120	A
Single Pulse Avalanche Energy ($L = 0.3\text{mH}$)	E_{AS}	273	mJ

Thermal Characteristics

Parameter	Symbol	Value	Unit
Power Dissipation ($T_c = 25^\circ\text{C}$)	P_D	150	W
Thermal Resistance Junction-to-Air	$R_{\theta JA}$	62	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	0.83	$^\circ\text{C/W}$
Operating Junction Temperature Range	T_J	$-55 \sim +150$	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	$-55 \sim +150$	$^\circ\text{C}$

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Electrical Characteristics (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	200	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 160V, V _{GS} = 0V	-	-	1	μA
		V _{DS} = 160V, V _{GS} = 0V, T _J = 125°C	-	-	50	
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
On Characteristics *3						
R _{DS(ON)}	Static Drain-Source On-resistance	V _{GS} = 10V,I _D = 30A	-	-	76	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	3	-	4.5	V
Dynamic Characteristics						
R _G	Gate Series Resistance	f = 1.0MHz	-	2.6	-	Ω
C _{ISS}	Input Capacitance	V _{GS} = 0V V _{DS} = 25V f = 1.0MHz	-	2444		pF
C _{OSS}	Output Capacitance		-	129		
C _{RSS}	Reverse Transfer Capacitance		-	24	-	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time	V _{DD} =100V V _{GS} = 10V R _G = 2.5Ω I _D = 30A	-	30	-	ns
t _r	Turn-on Rise Time		-	20	-	
t _{d(OFF)}	Turn-Off Delay Time		-	21	-	
t _f	Turn-Off Fall Time		-	31	-	
Q _G	Total Gate-Charge	V _{DD} = 100V I _D = 20A V _{GS} = 10V	-	53	-	nC
Q _{GS}	Gate to Source Charge		-	11	-	
Q _{GD}	Gate to Drain (Miller) Charge		-	16.5	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage	I _{SD} = 30A, V _{GS} = 0V	-	-	1.3	V
t _{rr}	Reverse recovery time	V _{GS} = 0V, I _F = 30A, di/dt = 100A/μs	-	50	-	ns
Q _{rr}	Reverse recovery charge		-	80	-	nC

Notes:

1. Silicon limited current only
2. Repetitive rating; pulse width limited by maximum junction temperature
3. The data tested by pulsed, pulse width $\leq 380\mu s$, duty cycle $\leq 2\%$

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Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

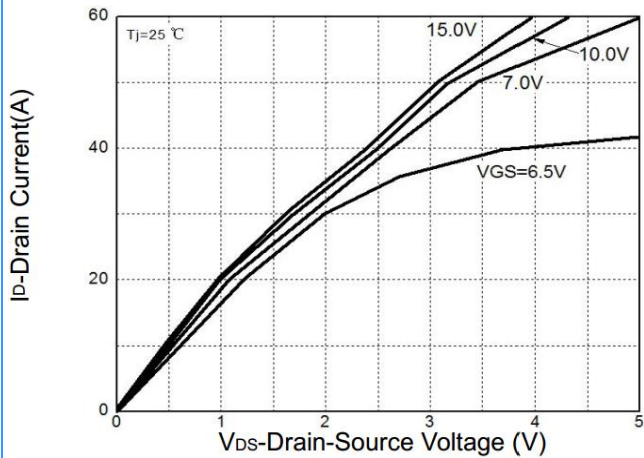


Fig 1 Typical Output Characteristics

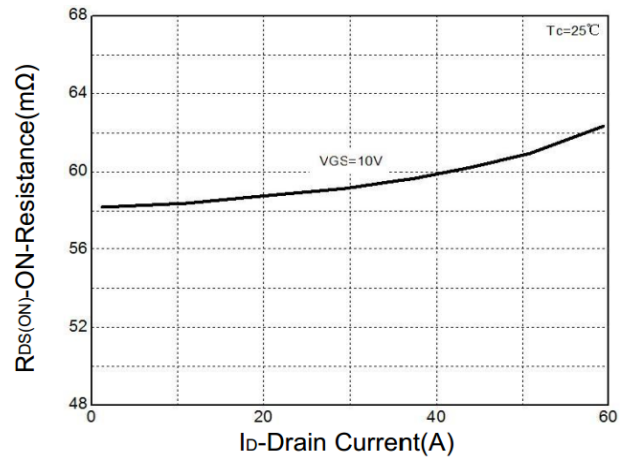


Fig 2 On-Resistance vs. Drain Current and Gate Voltage

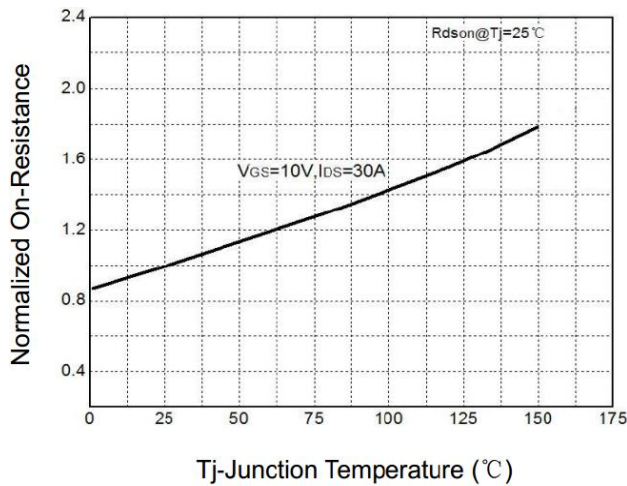


Fig 3 Normalized On-Resistance vs. Junction Temperature

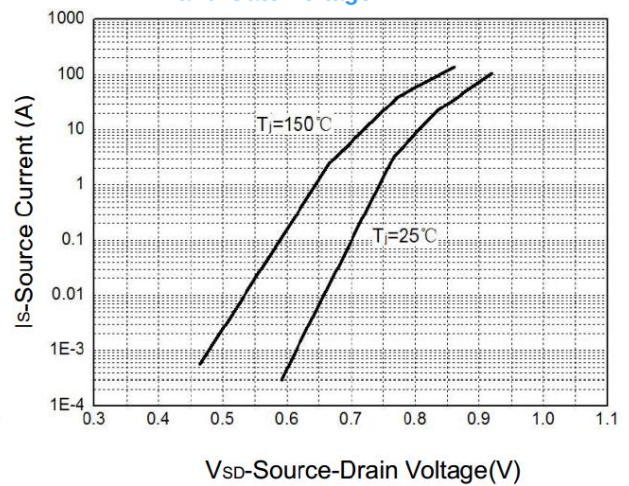


Fig 4 Body-Diode Characteristics

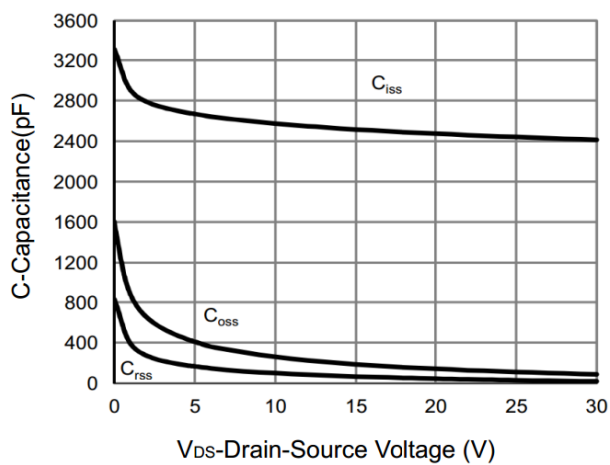


Fig 5 Capacitance Characteristics

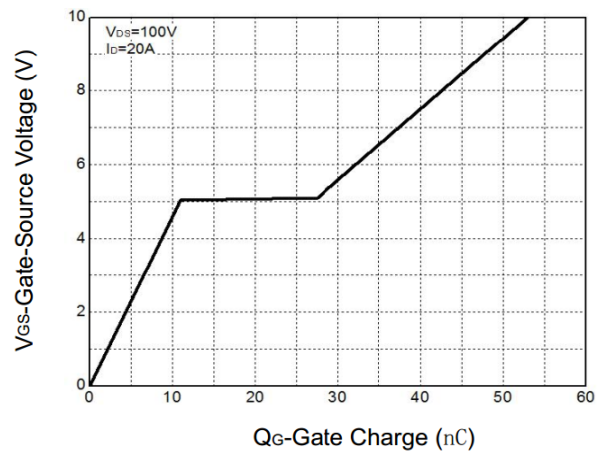


Fig 6 Gate-Charge Characteristics

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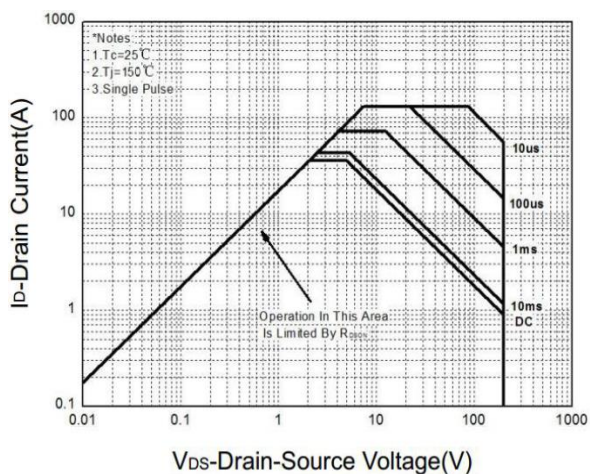


Fig 7 Safe Operation Area

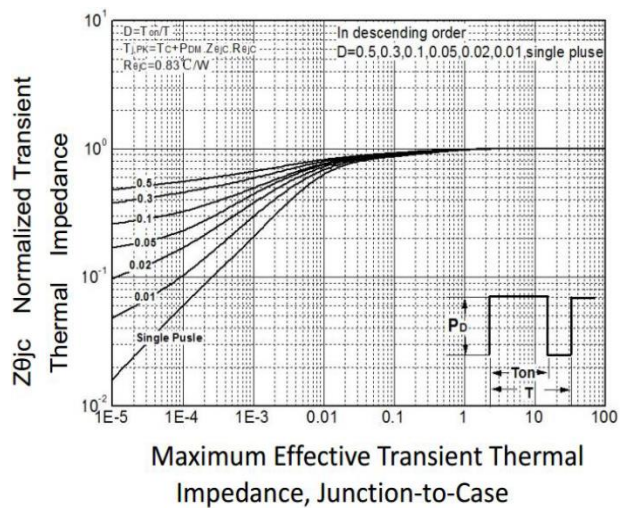
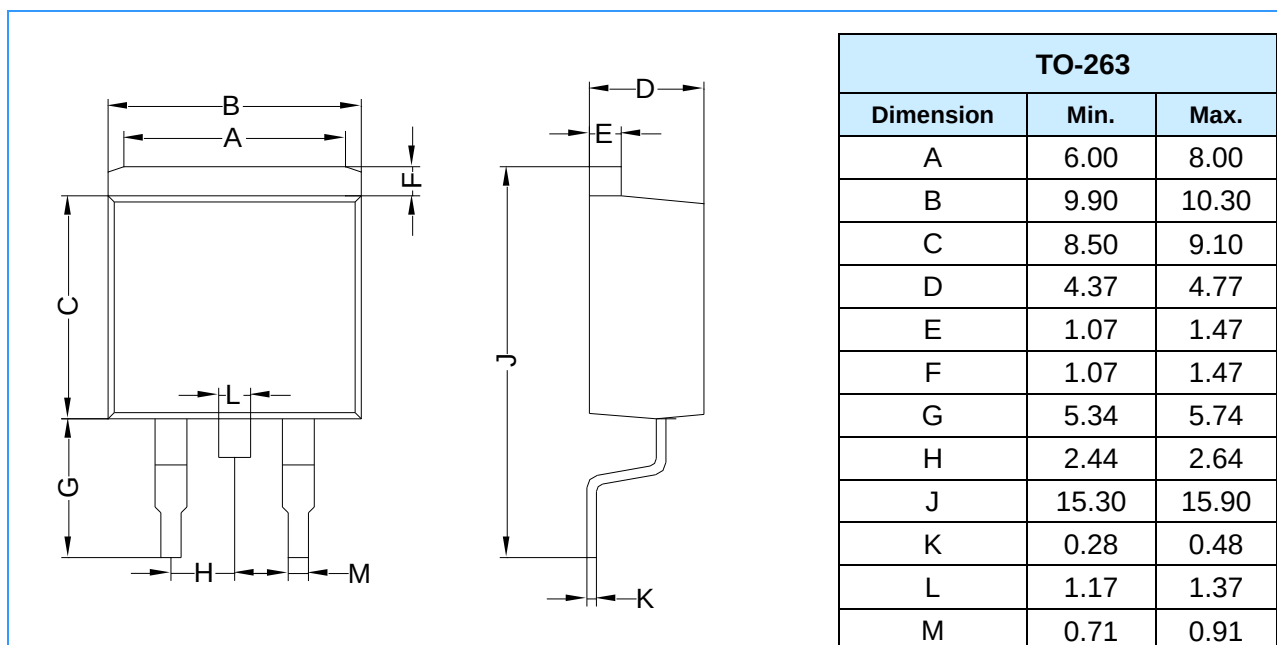


Fig 8 Thermal Transient Impedance

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Package Outline Dimensions (Unit: mm)



Mounting Pad Layout (Unit: mm)

